

#### Book Chapters:

Efe F. Orumwense, Thomas J. Afullo, and Viranjay M. Srivastava, "Cognitive radio networks: A social network perspective," *Advances in Nature and Biologically Inspired Computing (Advances in Intelligent Systems and Computing)*, Springer International Publishing, Switzerland, 2016,

#### Journals:

Himangi Sood, Viranjay M. Srivastava, and G. Singh, "Small signal modeling of scaled double-gate MOSFET for GHz applications," *J. of Microelectronics, Electronic Components and Materials*, vol. 47, no. 1, pp. 14 - 23, 2017

Ayodele S. Oluwole and Viranjay M. Srivastava, "Smart antenna for wireless communication systems using spatial signal processing," *J. of Communications (IJC)*, vol. 12, no. 6, pp. 328-339, June 2017.

Viranjay M. Srivastava, "Analysis and effects of voids in cylindrical surrounding double gate MOSFET for the RF Switches," *Nanoscience & Nanotechnology-Asia (NNA)*, vol. 7, no. 2, pp. 243-251, 2017.

Viranjay M. Srivastava, "Scaling effect on parameters of HfO<sub>2</sub> based CSDG MOSFET," *Int. J. of Engineering and Technology (IJET)*, vol. 9, no. 2, pp. 420-426, April-May 2017.

Kehinde O. Odeyemi, Pius A. Owolawi, and Viranjay M. Srivastava, "Performance analysis of free space optical system with spatial modulation and diversity combiners over the Gamma-Gamma atmospheric turbulence," *Optics Communications*, vol. 382, no. 1, pp. 205-211, Jan. 2017.

Efe F. Orumwense, Thomas J. Afullo, and Viranjay M. Srivastava, "Using massive MIMO and small cells to deliver a better energy-efficient cognitive radio networks," *Int. J. on Communications Antenna and Propagation (IRECAP)*, vol. 6, no. 5, pp. 274-281, Oct. 2016.

Himangi Sood, Viranjay M. Srivastava, and G. Singh, "Performance analysis of undoped and Gaussian doped cylindrical surrounding-gate MOSFET with its small signal modeling," *Microelectronics Journal (MEJ)*, vol. 57, pp. 66-75, Nov. 2016.

Olusegun O. Omitola and Viranjay M. Srivastava, "A robust speed-based handover algorithm for dense femtocell/macroc cell LTE-A network and beyond," *J. of Telecommunication, Electronic and Computer Engineering (JTECE)*, vol. 8, no. 9, pp. 121-129, Sept.-Dec. 2016.

Ayodele S. Oluwole and Viranjay M. Srivastava, "Design of smart antenna by circular pin-fed linearly polarized patch antenna," *Int. J. of Wireless and Microwave Technologies (IJWMT)*, vol. 6, no. 3, pp. 40-49, May 2016.

### Conferences:

E. O. Bolagun, Pius A. Owolawi, and Viranjay M. Srivastava, "Simulation and optimization of hybrid power systems for GSM and 4G base stations in South Africa, using the HOMER," IEEE Int. Conf. on AFRICON (IEEE AFRICON-2017), Cape Town, South Africa, 18-20 Sept. 2017.

Kehinde O. Odeyemi, Pius A. Owolawi, and Viranjay M. Srivastava, "Performance analysis of block error rate for SIM-FSO system with spatial diversity over Gamma-Gamma fading and pointing error channel," IEEE Int. Conf. on AFRICON (IEEE AFRICON-2017), Cape Town, South Africa, 18-20 Sept. 2017.

Kehinde O. Odeyemi, Pius A. Owolawi, and Viranjay M. Srivastava, "A comparison between mathematical tools for analyzing FSO systems over gamma-gamma atmospheric channel," IEEE Int. Conf. on AFRICON (IEEE AFRICON-2017), Cape Town, South Africa, 18-20 Sept. 2017.

Kehinde O. Odeyemi, Pius A. Owolawi, and Viranjay M. Srivastava, "Performance analysis of a dual-hop spatial modulation relaying system with spatial diversity over asymmetric RF/FSO channels," 20th Southern Africa Telecommunication Networks and Applications Conference (SATNAC-2017), Barcelona, Spain, 3-10 Sept. 2017.

Robson T. Reza and Viranjay M. Srivastava, "Effect of GSM Frequency Band on Received Signal Strength and Distance Estimation from Cell Tower," 10th Int. Conf. on Developments in e-Systems Engineering (DeSE 2017), Paris, France, 14-16 June 2017, pp. 1-4.

Dhawan Singh and Viranjay M. Srivastava, "Polarization Insensitive 3D Cylindrical Shaped Frequency Selective Surface," 10th Int. Conf. on Developments in e-Systems Engineering (DeSE 2017), Paris, France, 14-16 June 2017, pp. 1-5.

Uchechukwu A. Maduagwu and Viranjay M. Srivastava, "Rectifier with Cylindrical Surrounding Double-Gate MOSFET: A Model for Better Efficiency," 25th Int. Conf. on the Domestic Use of Energy (DUE 2017), Cape Town, South Africa, 3-5 April 2017, pp. 109-113.

Efe F. Orumwense, Thomas J. Afullo, and Viranjay M. Srivastava, "On increasing the energy efficiency of cognitive radio network base stations," 7th Annual Computing and Communication Workshop and Conference (CCWC), Las Vegas, USA, 9 -11 Jan. 2017, pp. 1-6.